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L1	3	"6714776".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:34
L2	1	"6304751".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:29
L3	1	"6285865".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:29
L4	1	"6268778".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:30
L5	1	"5994955".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:30
L6	1	"5994955".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:31
L7	1	"5950119".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:31
L8	1	"5870670".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:31
L9	1	"5563545".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:31
L10	1	"5563545".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:31
L11	1	"5428836".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:32
L12	1	"5400363".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:32
L13	1	"5390346".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:32
L14	1	"5321852".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:32
L15	1	"5311318".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:33
L16	1	"5311318".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:33
L17	1	"5303417".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:33
L18	1	"5140198".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:33
L19	1	"4953182".PN.	USPAT; USOCR	OR	OFF	2004/12/22 12:33
L20	38293	unbalanc??	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:35

L21	210066	balanc??	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:35
L22	5639	20 with 21	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:35
L23	92696	lpf or "low pass filter"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:35
L24	25	22 with 23	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:36
L25	507282	differential	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:37
L26	328	22 with 25	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:39
L27	14061	catv	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:37
L28	3	26 and 27	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:37
L29	2684	25 near3 21	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:39
L30	160	20 with 29	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:40
L31	589382	amplifier	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:40
L32	69243	25 adj 31	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:41

L33	495	22 and 32	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:41
L34	95	22 with 32	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:55
L35	267262	television	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:56
L36	5	34 and 35	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:26
L37	881292	cable or television	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:26
L38	36113	tuner	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:26
L39	3551	37 adj2 38	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:27
L40	36	22 and 39	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:33
L41	544251	video	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:33
L42	43	22 with 41	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:33
S1	3204	"cable modem"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:23
S2	33813	"intermediate frequency"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:16

S3	49143	"gain control"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:16
S4	6149	"lowpass filter"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:16
S5	19895777	@ad<"20001130"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:17
S6	5	S1 and S2 and S3 and S4 and S5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:17
S7	121989	"surface acoustic" or saw	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:20
S8	114	S2 and S3 and S4 and S7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:21
S9	1	S1 and S8	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:33
S10	479	"baseband filter"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:33
S11	11	S4 with S10	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:35
S12	22	S4 same S10	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:50
S13	3942	"dual gate"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:50
S14	60401	mosfet	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:51

S15	413	S13 with S14	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:51
S16	629893	balance\$	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S17	5	S15 with S16	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S18	361	S1 and S16	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S19	3267006	signal	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:18
S20	2524	S16 adj S19	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:19
S21	3	S1 and S20	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:21
S22	6855	qam	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:21
S23	1	S20 with S22	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:23
S24	36113	tuner	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:23
S25	10	S20 same S24	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:26
S26	14061	catv	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:26

S27	788	S24 with S26	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:26
S28	70	S7 and S27	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:29

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
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S2	99	Kuribayashi-hiroki.in.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/30 13:23
S3	24181	equalizer	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/30 13:23
S4	13	S2 and S3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/30 13:24
S5	447275	clock	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:32
S6	2702135	data	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:32
S7	195215	recover???	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:33
S8	4	domma	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:33
S9	4643	comma	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:33
S10	1173	S5 with S6 with S7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:33
S11	1884763	detect???	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:34
S13	0	S10 and S12	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:34

S14	23	S9 near S11	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/11/10 09:46
S15	266	"dual gate mosfet"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 15:15
S16	491883	balance	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 15:15
S17	182161	balanced	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 15:15
S18	613711	S16 or S17	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 15:15
S19	4	S15 with S18	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 15:19
S20	642713	amplifier or amplifying	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 15:20
S21	6	S15 adj S20	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 16:11
S22	6855	qam	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 16:11
S23	14	S16 with S22	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 16:12

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1 A comprehensive design method for dual-gate MOSFET mixers

Bergsma, A.J.; Syrett, B.A.;

Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Volume: 47 , Issue: 12 , Dec. 2000

Pages:1443 - 1451

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1 Analytical current model for dual-gate MOSFET

Karahalliloglu, K.; Dunder, G.;

Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International Conference on , Volume: 2 , 2-5 Sept. 2001

Pages:1015 - 1019 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) IEEE CNF

2 A 7-GHz 1.8-dB NF CMOS low-noise amplifier

Fujimoto, R.; Kojima, K.; Otaka, S.;

Solid-State Circuits, IEEE Journal of , Volume: 37 , Issue: 7 , July 2002

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[\[Abstract\]](#) [\[PDF Full-Text \(286 KB\)\]](#) IEEE JNL

3 A comprehensive design method for dual-gate MOSFET mixers

Bergsma, A.J.; Syrett, B.A.;

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4 Investigation of a novel self-aligned dual gate MOSFET structure

Roberds, B.E.; Whang, E.J.; Rudolph, A.; Doyle, B.S.;

SOI Conference, 1998. Proceedings., 1998 IEEE International , 5-8 Oct. 1998

Pages:109 - 110

[\[Abstract\]](#) [\[PDF Full-Text \(212 KB\)\]](#) IEEE CNF

5 Computer aided analysis of ESD effects in dual gate MOSFET VHF amplifier

Shastri, S.V.K.; Hariharan, V.K.;

Electromagnetic Compatibility, 1990. Symposium Record. 1990 IEEE International Symposium on , 21-23 Aug. 1990

Pages:424 - 430

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6 Subthreshold behavior and threshold voltages of short-channel dual-gate MOSFETs

Barsan, R.M.; Van De Wiele, F.;

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7 The effects of nitrogen implant into gate electrode on the characteristics of dual-gate MOSFETs with ultra-thin oxide and oxynitrides

Chou, A.I.; Lin, C.; Kumar, K.; Chowdhury, P.; Gardner, M.; Gilmer, M.; Fulford, J.; Lee, J.C.;

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International , 8-10 April 1997

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8 Monte Carlo simulation of a 30 nm dual-gate MOSFET: how short can Si go?

Frank, D.J.; Laux, S.E.; Fischetti, M.V.;

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9 Role of scattering in nanotransistors

Svizhenko, A.; Anantram, M.P.;

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10 To optimize electrical properties of the ultrathin (1.6 nm) nitride/oxide gate stacks with bottom oxide materials and post-deposition treatment

Chein-Hao Chen; Yean-Kuen Fang; Chih-Wei Yang; Shyh-Fann Ting; Yong-Shiuan Tsair; Ming-Fang Wang; Tuo-Hong Hou; Mo-Chiun Yu; Shih-Chang Chen; Jang, S.M.; Yu, D.C.H.; Mong-Song Liang;

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11 High performance characteristics in trench dual-gate MOSFET (TDMOS)

Mizuno, T.; Saitoh, Y.; Sawada, S.; Shinozaki, S.;

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12 Development of a low noise preamplifier for the detection and position determination of single electrons in a Cerenkov ring imaging detector by charge division

Spencer, E.; Coyle, P.; Williams, D.; Bienz, T.; Bird, F.; Gaillard, M.; Hallewell, G.; Kwon, Y.J.; Leith, D.; McShurley, D.; Oxoby, G.; Ratcliff, B.; Rensing, P.; Schultz, D.; Shapiro, S.; Toge, N.; Caldwell, D.; Lu, A.; Yellin, S.; Meadows, B.; Nussbaum, M.;

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13 A Behavioral Modeling Approach For 0.25pm Dual-gate Mosfet Design Optimization

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Papers. 1997 International Symposium on , June 3-5, 1997
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14 Design of DG-MOSFETs for high linearity performance

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Pages:68 - 69

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15 Room temperature operation of a single electron switch with an electrically formed quantum dot

Dae Hwan Kim; Dong-Hyuk Chae; Jong Duk Lee; Byung-Gook Park;
Device Research Conference Digest, 1999 57th Annual , 28-30 June 1999
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[\[Abstract\]](#) [\[PDF Full-Text \(164 KB\)\]](#) IEEE CNF

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